# DEVELOPMENT OF HIGHLY INTEGRATED W-BAND CIRCULATORS/ISOLATORS BASED ON SUBSTRATE INTEGRATED WAVEGUIDE (SIW) TECHNOLOGY

Henrik Kettunen<sup>(1)</sup>, Sampo Salo<sup>(2)</sup>, Huy Nguyen<sup>(3)</sup>, Juha Kainulainen<sup>(4)</sup>, Markku Lahti<sup>(5)</sup>, Mikko Kantanen<sup>(6)</sup>, Pekka Rantakari<sup>(7)</sup>, Léo Farhat<sup>(8)</sup>, Joaquín Jiménez<sup>(9)</sup>

> <sup>(1)</sup>Affiliation Harp Technologies Ltd, Tekniikantie 12, FI-02150 Espoo, Finland Email: henrik.kettunen@harptechnologies.com

> <sup>(2)</sup>Affiliation Harp Technologies Ltd, Tekniikantie 12, FI-02150 Espoo, Finland Email: sampo.salo@harptechnologies.com

> <sup>(3)</sup>Affiliation Harp Technologies Ltd, Tekniikantie 12, FI-02150 Espoo, Finland Email: huy.nguyen@harptechnologies.com

> <sup>(4)</sup>Affiliation Harp Technologies Ltd, Tekniikantie 12, FI-02150 Espoo, Finland Email: juha.kainulainen@harptechnologies.com

<sup>(5)</sup>Affiliation VTT, Kaitovayla 1, P.O. Box 1100, FI-90571 Oulu, Finland Email: markku.lahti@vtt.fi

> <sup>(6)</sup>Affiliation VTT, Tietotie 3, FI-02150 Espoo, Finland Email: mikko.kantanen@vtt.fi

> <sup>(7)</sup>Affiliation VTT, Tietotie 3, FI-02150 Espoo, Finland Email: pekka.rantakari@vtt.fi

<sup>(8)</sup>Affiliation ESA ESTEC, Keplerlaan 1, PO Box 299, NL-2200 AG Noordwijk, The Netherlands Email: leo.farhat@esa.int

<sup>(9)</sup>Affiliation ESA ESTEC, Keplerlaan 1, PO Box 299, NL-2200 AG Noordwijk, The Netherlands Email: joaquin.jimenez@ext.esa.int

# INTRODUCTION

The objective of the activity was to design, manufacture, and test highly integrated W-band circulators and isolators for frequency band 81 - 86 GHz using the Substrate Integrated Waveguide (SIW) technology. This recently emerged technology allows a direct integration of an effective rectangular waveguide inside the microstrip substrate of standard PCBs. Hence, it responds to the bulkiness, integrability and cost issues related to standard rectangular waveguide technology. SIW is considered an attractive choice for integrated communication and radar systems in space applications. For example, development of SIW circulators/isolators will allow their integration within RF chains and their implementation on the same board at lower cost. The use of SIW is expected to lead to a drastic reduction in mass, volume, and cost, and it should ease the assembly and integration of RF parts on one board compared to standard coaxial and waveguides solutions currently used in telecommunication at high frequencies.

In this activity, the SIW structures were chosen to be implemented using the Low Temperature Co-Fired Ceramic (LTCC) technology. LTCC allows constructing 3-dimensional structures within a ceramic substrate being hence very suitable for realizing especially substrate integrated waveguides

It is noteworthy that, in the literature, we did not find reports on any existing SIW circulators that would operate at Wband frequencies. The closest examples were designs for 38 GHz [1] and for 24 GHz [2], both implemented on a PCB substrate. For lower frequencies, LTCC based circulators were found, but they were fully based on microstrip topologies. Overall, no previous examples of their combinations, i.e., LTCC-SIW circulators, were found.

# DESIGN

A Y-junction circulator is a non-reciprocal 3-port component based on a ferrite resonator in the junction of three waveguide, or transmission line, arms. A photograph of designed and manufactured LTCC-SIW circulator is seen in Fig. 1. The permanent magnet on top of the junction creates a static H-field, which is required for biasing the ferrite into a correct mode of operation.

For probing purposes, the device terminals are equipped with additional transitions to 50  $\Omega$  Grounded Co-Planar Waveguide (GCPW) interfaces. The component has three GCPW ports, whose numbering is indicated in Fig. 1. The coplanar lines at ports 2 and 3 have additional 30-degree bends in order to align all ports on a rectangle, which makes the device probing more feasible and enhances the overall integrability.

A 2-port isolator is obtained by terminating one of the circulator ports. In practice, the planar LTCC technology requires the chosen termination to be integrated in the component. In our case, the most elegant and symmetrical choice is to terminate the circulator port 1. The remaining ports 2 and 3 are re-numbered as port 1 and port 2, as seen in Fig. 2, which presents two studied variants of SIW isolators. In the first variant, the termination is implemented with a discrete  $50 \Omega$  thin film resistor and in the second one, the termination is based on a distributed absorber, which is manufactured by LTCC technology using conductive paste. The inner structure of the isolators is identical to the circulator design.

The LTCC structure consists of a stack of dielectric layers, which are sintered together into a uniform substrate. That is, the thickness of the substrate can only be varied in discrete steps. However, thin conductor layers can be sintered inside the substrate in between the ceramic layers. These conductor layers are connected by vias, which are also used to form the sidewalls of the effective, substrate integrated, waveguides.

The most essential element in the circulator is the biased ferrite disk, which is located in the middle of the Y-junction. In order to enable the assembly of the ferrite after the LTCC process, the ferrite is placed against the bottom of the component surrounded by an air cavity with a slightly larger radius. A piece of copper tape is used to seal the cavity. The bottom of the component is supplemented with a steel plate. Steel has very high (nonlinear) permeability and hence the plate at the bottom behaves as a magnetic mirror, which attracts the magnetic field lines and enhances the applied flux in the vicinity of the ferrite. A cross-cut through the junction is illustrated in Fig. 3.



Fig. 1. LTCC-SIW Y-junction circulator.



Fig. 2. Left: LTCC-SIW isolator with discrete resistor termination. Right: LTCC-SIW isolator with distributed absorber termination.

# SIMULATIONS AND MEASUREMENTS

The simulated 3-port S-parameters of the designed LTCC-SIW circulator are plotted in Fig. 4.

Unfortunately, major issues in the measured performance of the manufactured circulators and isolators were observed. The level of matching turned out to be very poor and the required nonreciprocity was nearly completely missing. Some typical S-parameter measurement results are presented in the following.

Altogether 11 samples of circulators were assembled and tested. They all provided very similar results. Due to practical limitations, the circulator 3-port parameters were measured as 3 separate 2-port measurements, where the third port was terminated with a matched load (see the port numbers in Fig.1). The results of a typical sample are presented in Fig. 5.

Fig. 6 presents typical 2-port measurement results for both isolator variants. It is found that these results are more stable and more similar to each other, even though their performance is still far from the desired specifications. The similarity between the isolator variants indicates that the integrated terminations based on a discrete resistor and distributed absorber seem to work equally well. The termination in the third port, however, seems to affect the matching in the signal ports, as well. Whereas for the circulator, the best matching is observed around 81 - 82 GHz (see Fig. 5), for isolators this is observed close to 85 GHz.



Fig. 3. Cross-cut of the circulator/isolator junction assembly.



Fig. 4. Simulated 3-port S-parameters of LTCC-SIW circulator.



Fig. 5. Typical measured S-parameters of manufactured LTCC-SIW circulator. Top left: 2-port measurement between P1 and P2 with P3 terminated. Top right: 2-port measurement between P2 and P3 with P1 terminated. Bottom: 2-port measurement between P3 and P1 with P2 terminated.



Fig. 6. 2-ports S-parameter measurements of typical samples of LTCC -SIW isolator. Left: variant with discrete resistor. Right: variant with distributed absorber.

## TROUBLESHOOTING

By specific measurements, we were able to rule out the problems in the ferrites and the magnets. It was hence deduced that the issues must be related to the various sensitivities and tolerances of the LTCC process and the manual assembly of the ferrite. The differences between circulators and isolators also indicate that there are potential issues with the GCPW interface matching and termination.

Eventually, the most apparent problem was found to be the mechanical assembly of the ferrite to the LTCC junction. Additional tests were made to improve the quality of the junction assembly, which was illustrated in Fig. 3. Some silver epoxy was added also under the ferrite to improve the galvanic contact with the steel plate (the copper tape was removed at this point). Fig. 7 presents a comparison between the original (P2-P3 2-port) measurement and an updated measurement after the same component was re-assembled using silver epoxy. It is found that the circulator matching becomes significantly improved by the additional epoxy, and the insertion losses are much smaller. Even though the gyrotropy effect also becomes more prominent, the isolation, seen as difference between  $S_{32}$  and  $S_{23}$ , unfortunately remains rather low.



Fig. 7. 2-port measurement of the same circulator sample. Left: Original measurement. Right: measurement after reassembly of the component with additional silver epoxy to improve the ferrite contacts.

### CONCLUSIONS

After two manufacturing rounds and manufacturing process assessment and optimization, the designed and manufactured LTCC-SIW circulators and isolators did not yet perform according to specifications. Notable differences between simulated and measured performance were observed, but a single unambiguous root cause for these discrepancies was, however, not found.

The most apparent problem is the mechanical assembly of the ferrite to the LTCC junction. The design model assumes the ferrite bottom and top have both a good galvanic contact with conducting surfaces, that is, the ferrite disk is supposed to act as a resonator, which is short-circuited from both ends. Based on the studies of realized dimensions, this condition is very difficult to achieve. The achievable machining and assembly tolerances simply just do not meet the requirements. After a careful junction re-assembly with additional silver epoxy, a very promising matching was obtained within a broader band at correct frequencies. Nevertheless, the main features of the similar and measured curves are rather similar.

As light-weight and highly integrable components, SIW circulators and isolators appear very attractive and costeffective for many space applications. Further design and optimization rounds are, however, needed to improve the current circulator/isolator performance. Considering possible future steps, a more feasible approach could be to try to repeat the design task for a notably lower frequency. This would make the physical dimensions larger and mitigate the required tolerances in manufacturing and assembly. Moreover, the efficiency of ferrites is supposedly better at lower frequencies. For instance, LEO inter satellite links at V-band between 59 - 71 could be a potential application.

## REFERENCES

- H.-T. Chou, C.-H. Chang, and Y.-T. Chen, "Ferrite Circulator Integrated Phased-Array Antenna Module for Dual-Link Beamforming at Millimeter Frequencies," *IEEE Transactions on Antennas and Propagation*, vol. 66, no. 11, pp. 5934-5942, Nov. 2018.
- [2] W. D'Orazio, K. Wu, "Substrate-Integrated-Waveguide Circulators Suitable for Millimeter-Wave Integration," *IEEE Transactions on Microwave Theory and Techniques*, vol. 54, issue 10, pp. 3675-3680, Oct. 2006.

#### AUTHORS

**Henrik Kettunen** was born in Orimattila, Finland, in 1980. He received the M.Sc. (Tech.) and Lic.Sc. (Tech.) degrees in Electrical Engineering from the Helsinki University of Technology (TKK), Espoo, Finland, in 2006 and 2009, respectively, and the D.Sc. (Tech.) degree in Electrical Engineering from the Aalto University School of Electrical Engineering (formerly TKK), Espoo, Finland, in 2011. He is currently working as a Senior Design Engineer focusing on ferrite-based components at Harp Technologies Ltd, in Espoo, Finland.

**Sampo Salo** was born in Jyväskylä, Finland, in 1989. He received the M.Sc. (Tech.) degree in Electrical Engineering in Aalto University, Espoo, Finland, in 2016. He is currently working as a Business Line Manager, Microwave components, at Harp Technologies Ltd, Espoo, Finland.

**Huy Nguyen** was born in Ho Chi Minh City, Vietnam, in 1990. He received the M.Sc. (Tech.) degree in Radio Science and Engineering from the Aalto University School of Electrical Engineering (formerly TKK), Espoo, Finland, in 2016. He is currently the Principal Design Engineer, Ferrite Products at Harp Technologies Oy, Finland with experience in the technology developments and designs of various ferrite components, especially switches, circulators, phase shifters, in various frequency bands including micro- and millimeter waves.

**Juha Kainulainen** was born in Lappajärvi, Finland, in 1979. He received the M.Sc. (Tech.) degree in technology from the Helsinki University of Technology (currently part of the Aalto University), Espoo, Finland, in 2004, and D.Sc. (Tech.) degree from Aalto University in 2013. Since 2012 he has been a Principal Scientist of Harp Technologies Oy, Finland. His duties include acting as Project Manager and Chief System Engineer in micro- and millimeter wave related technology developments and system studies in the field of remote sensing, signal processing, and space technologies, and leading the Signal Processing business line of Harp Technologies.

**Markku Lahti** received his M.Sc. and D.Sc. degrees from the University of Oulu, Finland, in 1993 and 2008, respectively. He is working as a Senior Scientist at the VTT Technical Research Centre of Finland. His main research interests include the manufacturing of ceramic packages and integration of photonic and electronic components.

**Mikko Kantanen** received the M.Sc., Lic.Sc., and D.Sc. degrees in Electrical Engineering from Aalto University (formerly Helsinki University of Technology), Espoo, Finland 2001, 2006, and 2017, respectively. Since 2001 he has worked in VTT Technical Research Centre of Finland Ltd., Espoo, Finland, currently as a Senior Scientist. His research interests include millimeter-wave integrated circuit design, millimeter-wave measurements, and millimeter-wave systems. Dr. Kantanen is a recipient of a 47th European Microwave Conference Microwave Prize and a co-recipient of an Asia-Pacific Microwave Conference 2006 Prize.

**Pekka Rantakari** received the M.Sc. degree in electrical engineering from Helsinki University of Technology, Espoo, Finland, in 2004. Since 2005, he has been with the Millimetre Wave Laboratory of Finland-MilliLab, VTT Technical Research Center of Finland, Espoo, where he currently works as a Research Team Leader.

**Léo Farhat** received his M.Sc. degree in Engineering in Materials and EEE Devices for Communications Systems from the University of West Brittany, France, in 2007. He earned his PhD in Electrical, Electronics, and Communications Engineering from the Microwave department of Telecom Bretagne, France, in 2010. Currently, he works as a Passive EEE Component Expert at the European Space Agency (ESA). With over 16 years of experience, he specializes in the development, qualification, and application of passive and RF passive components for space applications.

**Joaquín Jiménez** got a Master Degree in Telecommunications Engineering at the University of Seville (Spain). He started his career as EEE components test engineer at the Parts Laboratory department at ALTER TECHNOLOGY (Seville, Spain). He continued also as EEE component test engineer at the ESA Materials and Electrical Components Laboratory at ESTEC (Noordwijk, The Netherlands). Since November 2020 he works as Passive EEE component engineer at the ESA's Component Section, together with Léo Farhat.